

REMARKS

This paper is responsive to the Office action mailed on February 21, 2006. Favorable reconsideration is requested.

Claims 1-61 are in the application. The examiner requested a showing of support in the original description as suggested in MPEP 2163 page 2100-183 lines 3-7 of the right column, for previously presented claims 28-61, which were new claims as of 11/25/05. Claims 28-61 are fully supported by the original specification, and no new matter has been entered. Following remarks illustrate the support in the original description for the previously presented claims 28-61 without limitations. The support for these claims may appear in multiple places in the original disclosure and hence the remarks below should not be used as limiters.

Independent Claim 28:

The security system comprising a network of the claim is first discussed in the original description on page 4, line 31 through page 7 line 16.

The network comprising one or more networked systems of one or more types is discussed in various sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The network systems comprising a hardware processor providing transport layer protocol processing is described also in multiple sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, as well as Description section on page 12 line 10.

The hardware processor comprising a protocol processing engine for performing transport layer protocol processing is described again in multiple sections like those above as well as in the detailed description section starting from page 26 line 13 through page 30 line 13.

The security system providing multiple protocol layer security is also illustrated in multiple sections of the original description like on page 6 line 13.

Dependent Claim 29:

The hardware processor further comprising a programmable rule processing engine for analyzing network traffic for security rule matching and the like is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A security processing engine for performing encryption, decryption and the like is described on page 43 line 17 through page 44 line 10.

A packet classification engine for classifying the network traffic is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A packet processing engine for performing packet processing tasks is similarly described in various sections along with page 9 lines 10 through 17; page 27 line 29 through page 28 line 24; page 34 line 18 through page 35 line 12.

Dependent Claim 30:

Header processing and deep packet processing is described again in many sections of the original description including Lines 19 through 26 on page 31 as well as lines 2 through 24 of page 36.

Independent Claim 31:

The security system for a storage area network comprising a network of the claim is discussed in the original description on page 11 line 21, page 14 lines 18 through 32, page 15 lines 1 through 25, page 61 line 13 through page 63 line 24 as well as page 4, line 31 through page 7 line 16.

The security system comprising a set of systems form one or more networked systems is discussed in various sections like the Background of the Invention section starting page 1 line

17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The set of systems comprising a hardware processor providing transport layer protocol processing is described also in multiple sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, as well as Description section on page 12 line 10.

The hardware processor comprising a protocol processing engine for performing transport layer protocol processing is described again in multiple sections like those above as well as in the detailed description section starting from page 26 line 13 through page 30 line 13.

The security system providing multiple protocol layer security is also illustrated in multiple sections of the original description like on page 6 line 13.

Dependent Claim 32:

The hardware processor further comprising a storage protocol processing engine is described in multiple sections comprising lines 10-20 page 9; lines 14 of page 27 through line 24 of page 28; line 1 of page 38 through line 14 of page 40.

The hardware processor further comprising a programmable rule processing engine for analyzing storage area network traffic for security rule matching and the like is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A security processing engine for performing encryption, decryption and the like is described on page 43 line 17 through page 44 line 10.

A packet classification engine for classifying the storage area network traffic is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A packet processing engine for performing packet processing tasks is similarly described in various sections along with page 9 lines 10 through 17; page 27 line 29 through page 28 line 24; page 34 line 18 through page 35 line 12.

Dependent Claim 33:

The security system comprising multiple protocol layer security that comprises security functions performed at one or more protocol layers of OSI stack is described on page 6 lines 12-14; page 63 line 24 through page 69 line 20; and page 12 line 20 through page 15 line 25.

Independent Claim 34:

The security system comprising a network of the claim is first discussed in the original description on page 4, line 31 through page 7 line 16.

The network comprising one or more networked systems of one or more types is discussed in various sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The network systems comprising a hardware processor providing remote direct memory access capability is described in multiple sections like Brief Description of Figures section starting page 8 line 10 thru page 11 line 5; page 13 lines 1 through page 16 line 6; page 20 line 6 through page 21 line 5; page 26 line 13 through page 29 line 30; as well as page 37 line 4 through page 40 line 6.

The hardware processor comprising an RDMA mechanism for performing RDMA data transfers is described amongst multiple sections like starting on page 20 line 6 through page 21 line 5; page 29 line 5 through page 29 line 30.

The security system providing multiple protocol layer security is also illustrated in multiple sections of the original description like on page 6 line 13.

Dependent Claim 35:

The hardware processor comprising a protocol processing engine providing transport layer protocol processing is described also in multiple sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, as well as Description section on page 12 line 10.

The hardware processor further comprising a programmable rule processing engine for analyzing network traffic for security rule matching and the like is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A security processing engine for performing encryption, decryption and the like is described on page 43 line 17 through page 44 line 10.

A packet classification engine for classifying the network traffic is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A packet processing engine for performing packet processing tasks is similarly described in various sections along with page 9 lines 10 through 17; page 27 line 29 through page 28 line 24; page 34 line 18 through page 35 line 12.

Dependent Claim 36:

Header processing and deep packet processing is described again in many sections of the original description including Lines 19 through 26 on page 31 as well as lines 2 through 24 of page 36.

Dependent Claim 37:

The hardware processor providing a transport layer remote direct memory access (RDMA) capability is described amongst multiple sections like starting on page 20 line 6 through page 21 line 5; as well as page 29 line 5 through page 29 line 30.

Dependent Claim 38:

The security system comprising multiple protocol layer security that comprises security functions performed at one or more protocol layers of OSI stack is described on page 6 lines 12-14; page 63 line 24 through page 69 line 20; and page 12 line 20 through page 15 line 25.

Dependent Claim 39:

The security system comprising multiple protocol layer security that comprises security functions performed at one or more protocol layers of OSI stack is described on page 6 lines 12-14; page 63 line 24 through page 69 line 20; and page 12 line 20 through page 15 line 25.

Independent Claim 40:

The security system comprising a network of the claim is first discussed in the original description on page 4, line 31 through page 7 line 16.

The network comprising one or more networked systems of one or more types is discussed in various sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The network systems comprising a remote direct memory access capability is described in multiple sections like Brief Description of Figures section starting page 8 line 10 thru page 11 line 5; page 13 lines 1 through page 16 line 6; page 20 line 6 through page 21 line 5; page 26 line 13 through page 29 line 30; as well as page 37 line 4 through page 40 line 6.

The security system providing multiple protocol layer security is also illustrated in multiple sections of the original description like on page 6 line 13.

Dependent Claim 41:

The RDMA capability comprising a hardware processor comprising remote direct memory access mechanism is described in multiple sections like Brief Description of Figures section starting page 8 line 10 thru page 11 line 5; page 13 lines 1 through page 16 line 6; page

20 line 6 through page 21 line 5; page 26 line 13 through page 29 line 30; as well as page 37 line 4 through page 40 line 6.

Dependent Claim 42:

The RDMA capability comprising a hardware processor that provides a transport layer remote direct memory access mechanism is described in multiple sections like Brief Description of Figures section starting page 8 line 10 thru page 11 line 5; page 13 lines 1 through page 16 line 6; page 20 line 6 through page 21 line 5; page 26 line 13 through page 29 line 30; as well as page 37 line 4 through page 40 line 6.

Dependent Claim 43:

The hardware processor further comprising a protocol processing engine providing transport layer protocol processing is described also in multiple sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, as well as Description section on page 12 line 10.

The hardware processor further comprising a programmable rule processing engine for analyzing network traffic for security rule matching and the like is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A security processing engine for performing encryption, decryption and the like is described on page 43 line 17 through page 44 line 10.

A packet classification engine for classifying the network traffic is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A packet processing engine for performing packet processing tasks is similarly described in various sections along with page 9 lines 10 through 17; page 27 line 29 through page 28 line 24; page 34 line 18 through page 35 line 12.

Dependent Claim 44:

Header processing and deep packet processing is described again in many sections of the original description including Lines 19 through 26 on page 31 as well as lines 2 through 24 of page 36.

Independent Claim 45:

The security system comprising a storage area network of the claim is discussed in the original description on page 11 line 21, page 14 lines 18 through 32, page 15 lines 1 through 25, page 61 line 13 through page 63 line 24 as well as page 4, line 31 through page 7 line 16.

The storage area network comprising one or more networked systems of one or more types is discussed in various sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The network systems comprising a remote direct memory access capability is described in multiple sections like Brief Description of Figures section starting page 8 line 10 thru page 11 line 5; page 13 lines 1 through page 16 line 6; page 20 line 6 through page 21 line 5; page 26 line 13 through page 29 line 30; as well as page 37 line 4 through page 40 line 6.

The security system providing multiple protocol layer security is also illustrated in multiple sections of the original description like on page 6 line 13.

Dependent Claim 46:

The RDMA capability comprising a hardware processor comprising remote direct memory access mechanism is described in multiple sections like Brief Description of Figures section starting page 8 line 10 thru page 11 line 5; page 13 lines 1 through page 16 line 6; page 20 line 6 through page 21 line 5; page 26 line 13 through page 29 line 30; as well as page 37 line 4 through page 40 line 6.



Dependent Claim 47:

The RDMA capability comprising a hardware processor providing a transport layer remote direct memory access capability is described in multiple sections like Brief Description of Figures section starting page 8 line 10 thru page 11 line 5; page 13 lines 1 through page 16 line 6; page 20 line 6 through page 21 line 5; page 26 line 13 through page 29 line 30; as well as page 37 line 4 through page 40 line 6.

Dependent Claim 48:

The hardware processor further comprising a storage protocol processing engine is described in multiple sections comprising lines 10-20 page 9; lines 14 of page 27 through line 24 of page 28; line 1 of page 38 through line 14 of page 40.

The hardware processor further comprising a protocol processing engine for transport layer protocol processing is described also in multiple sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, as well as Description section on page 12 line 10.

The hardware processor further comprising a programmable rule processing engine for analyzing storage area network traffic for security rule matching and the like is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A security processing engine for performing encryption, decryption and the like is described on page 43 line 17 through page 44 line 10.

A packet classification engine for classifying the storage area network traffic is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A packet processing engine for performing packet processing tasks is similarly described in various sections along with page 9 lines 10 through 17; page 27 line 29 through page 28 line 24; page 34 line 18 through page 35 line 12.

Dependent Claim 49:

Header processing and deep packet processing is described again in many sections of the original description including Lines 19 through 26 on page 31 as well as lines 2 through 24 of page 36.

Independent Claim 50:

The security system comprising a network of the claim is first discussed in the original description on page 4, line 31 through page 7 line 16.

The network comprising one or more networked systems of one or more types is discussed in various sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The network systems comprising a hardware processor providing transport layer protocol processing is described also in multiple sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, as well as Description section on page 12 line 10.

The hardware processor comprising a protocol processing engine for performing transport layer protocol processing is described again in multiple sections like those above as well as in the detailed description section starting from page 26 line 13 through page 30 line 13.

The hardware processor further comprising a programmable rule processing engine for analyzing network traffic for security rule matching and the like is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

The security system providing multiple protocol layer security is also illustrated in multiple sections of the original description like on page 6 line 13.

Independent Claim 51:

The security system comprising a network of the claim is first discussed in the original description on page 4, line 31 through page 7 line 16.

The network comprising one or more networked systems of one or more types is discussed in various sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The network systems comprising a hardware processor providing transport layer protocol processing is described also in multiple sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, as well as Description section on page 12 line 10.

The hardware processor comprising a protocol processing engine for performing transport layer protocol processing is described again in multiple sections like those above as well as in the detailed description section starting from page 26 line 13 through page 30 line 13.

The hardware processor further comprising a programmable rule processing engine for analyzing network traffic for security rule matching and the like is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A security processing engine for performing encryption, decryption and the like is described on page 43 line 17 through page 44 line 10.

The security system providing multiple protocol layer security is also illustrated in multiple sections of the original description like on page 6 line 13.

Independent Claim 52:

The security system for a storage area network of the claim is discussed in the original description on page 11 line 21, page 14 lines 18 through 32, page 15 lines 1 through 25, page 61 line 13 through page 63 line 24 as well as page 4, line 31 through page 7 line 16.

The storage area network comprising one or more networked systems of one or more types is discussed in various sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The security system comprising a set of systems from one or more networked systems is discussed in various sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The plurality of said set of systems comprising a hardware processor providing transport layer protocol processing is described also in multiple sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, as well as Description section on page 12 line 10.

The hardware processor comprising a protocol processing engine for performing transport layer protocol processing is described again in multiple sections like those above as well as in the detailed description section starting from page 26 line 13 through page 30 line 13.

The hardware processor comprising a storage protocol processing engine is described in multiple sections comprising lines 10-20 page 9; lines 14 of page 27 through line 24 of page 28; line 1 of page 38 through line 14 of page 40.

The security system providing multiple protocol layer security is also illustrated in multiple sections of the original description like on page 6 line 13.

Dependent Claim 53:

The hardware processor further comprising a programmable rule processing engine for analyzing storage area network traffic for security rule matching and the like is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A security processing engine for performing encryption, decryption and the like is described on page 43 line 17 through page 44 line 10.

A packet classification engine for classifying the storage area network traffic is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A packet processing engine for performing packet processing tasks is similarly described in various sections along with page 9 lines 10 through 17; page 27 line 29 through page 28 line 24; page 34 line 18 through page 35 line 12.

Dependent Claim 54:

Header processing and deep packet processing is described again in many sections of the original description including Lines 19 through 26 on page 31 as well as lines 2 through 24 of page 36.

Independent Claim 55:

The security system comprising a network of the claim is first discussed in the original description on page 4, line 31 through page 7 line 16.

The network comprising one or more networked systems of one or more types is discussed in various sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The network systems comprising a hardware processor providing remote direct memory access capability is described in multiple sections like Brief Description of Figures section starting page 8 line 10 thru page 11 line 5; page 13 lines 1 through page 16 line 6; page 20 line 6 through page 21 line 5; page 26 line 13 through page 29 line 30; as well as page 37 line 4 through page 40 line 6.

The hardware processor comprising an RDMA mechanism for performing RDMA data transfers is described amongst multiple sections like starting on page 20 line 6 through page 21 line 5; page 29 line 5 through page 29 line 30.

The hardware processor comprising a protocol processing engine for performing transport layer protocol processing is described again in multiple sections like those above as well as in the detailed description section starting from page 26 line 13 through page 30 line 13.

The security system providing multiple protocol layer security is also illustrated in multiple sections of the original description like on page 6 line 13.

Dependent Claim 56:

The hardware processor further comprising a programmable rule processing engine for analyzing network traffic for security rule matching and the like is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A security processing engine for performing encryption, decryption and the like is described on page 43 line 17 through page 44 line 10.

A packet classification engine for classifying the network traffic is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A packet processing engine for performing packet processing tasks is similarly described in various sections along with page 9 lines 10 through 17; page 27 line 29 through page 28 line 24; page 34 line 18 through page 35 line 12.

Dependent Claim 57:

Header processing and deep packet processing is described again in many sections of the original description including Lines 19 through 26 on page 31 as well as lines 2 through 24 of page 36.

Independent Claim 58:

The security system for a storage area network of the claim is discussed in the original description on page 11 line 21, page 14 lines 18 through 32, page 15 lines 1 through 25, page 61 line 13 through page 63 line 24 as well as page 4, line 31 through page 7 line 16.

The storage area network comprising one or more networked systems of one or more types is discussed in various sections like the Background of the Invention section starting page 1 line 17; the Summary of Invention section starting page 2 line 19; Brief Description of Figures section starting page 7 line 27, page 11 line 18; as well as Description section starting on page 12 line 5.

The plurality of said one or more networked systems comprising a hardware processor providing remote direct memory access capability is described in multiple sections like Brief Description of Figures section starting page 8 line 10 thru page 11 line 5; page 13 lines 1 through page 16 line 6; page 20 line 6 through page 21 line 5; page 26 line 13 through page 29 line 30; as well as page 37 line 4 through page 40 line 6.

The hardware processor comprising an RDMA mechanism for performing RDMA data transfers is described amongst multiple sections like starting on page 20 line 6 through page 21 line 5; page 29 line 5 through page 29 line 30.

The security system providing multiple protocol layer security is also illustrated in multiple sections of the original description like on page 6 line 13.

Dependent Claim 59:

The hardware processor comprising a storage protocol processing engine is described in multiple sections comprising lines 10-20 page 9; lines 14 of page 27 through line 24 of page 28; line 1 of page 38 through line 14 of page 40.

The hardware processor comprising a protocol processing engine for performing transport layer protocol processing is described again in multiple sections like those above as well as in the detailed description section starting from page 26 line 13 through page 30 line 13.

The hardware processor further comprising a programmable rule processing engine for analyzing network traffic for security rule matching and the like is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A security processing engine for performing encryption, decryption and the like is described on page 43 line 17 through page 44 line 10.

A packet classification engine for classifying the network traffic is described on page 6 lines 10-14, lines 23-31; on page 7 lines 19-21; line 8 of page 9; line 1 page 10; lines 1-11 of page 27; page 33 line 7 through page 34 line 17.

A packet processing engine for performing packet processing tasks is similarly described in various sections along with page 9 lines 10 through 17; page 27 line 29 through page 28 line 24; page 34 line 18 through page 35 line 12.

Dependent Claim 60:

The hardware processor where in the packet processing comprises header processing and deep packet processing is described again in many sections of the original description including Lines 19 through 26 on page 31 as well as lines 2 through 24 of page 36.

Dependent Claim 61:

The hardware processor where in the packet processing tasks comprises header processing and deep packet processing is described again in many sections of the original description including Lines 19 through 26 on page 31 as well as lines 2 through 24 of page 36.

In view of these remarks, applicant asserts claims 28-61 are fully supported by the original specification and that no new matter has been added. Applicant also believes that all claims are allowable and requests the claims be passed to issue.

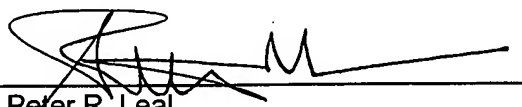


Appl. No.: 10/783,890  
Docket No.: 2103110-991180  
Amendment

The Commissioner is hereby authorized to charge any additional fees or credit any overpayment that may be associated with this communication to Deposit Account No. 07-1896.

Respectfully submitted,  
DLA PIPER RUDNICK GRAY CARY US LLP

Dated: March 17, 2006

By:   
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